**VIDHI ROHIRA**

**SY BTECH**

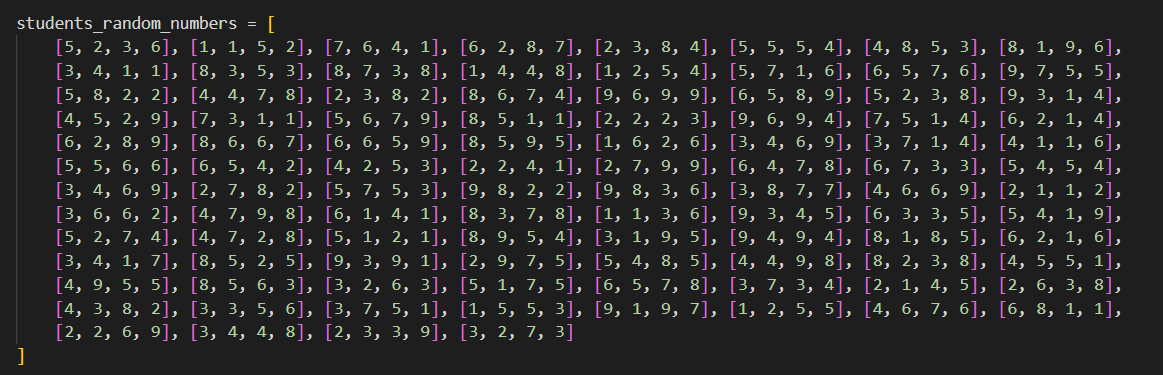
**COMPUTER ENGINEERING**

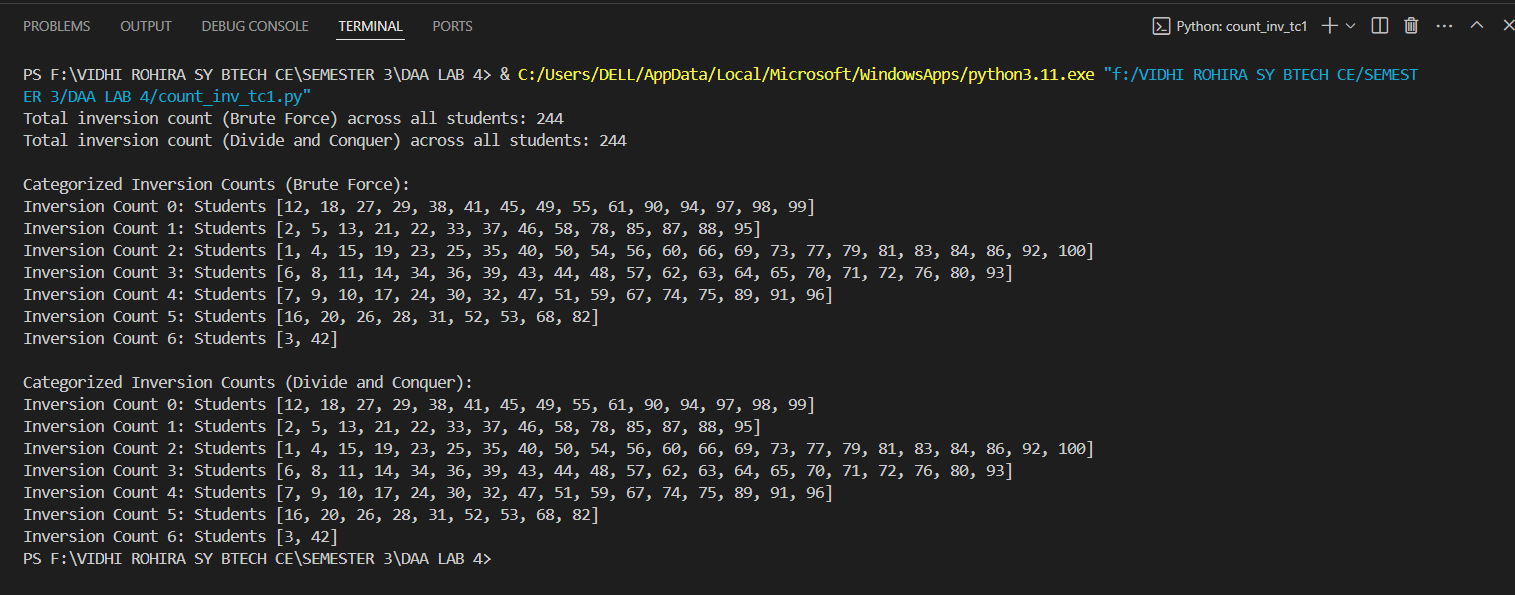
**DAA LAB 4**

**OUTPUTS**

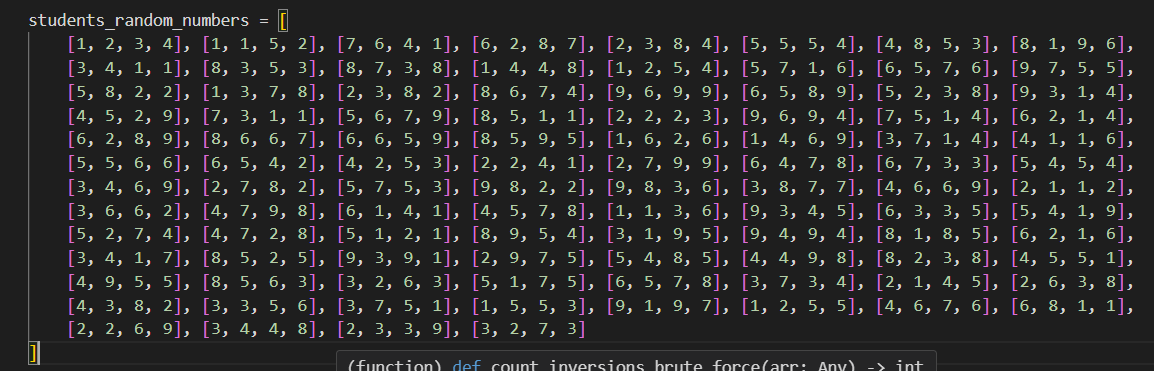
**231071052**

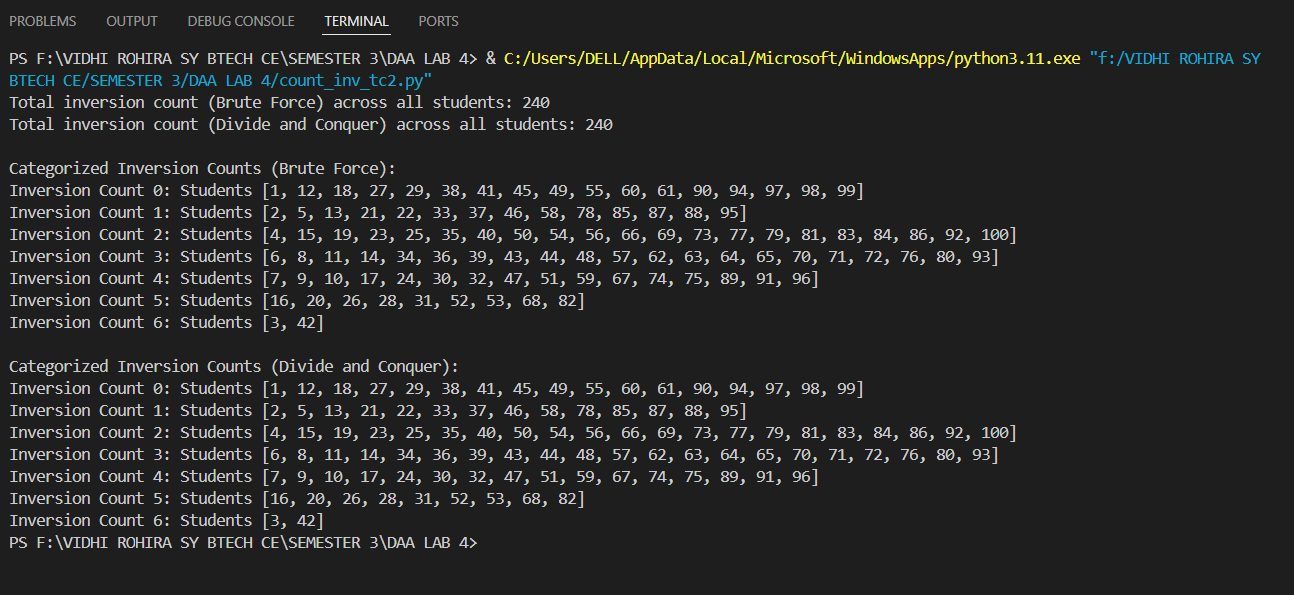
**TEST CASE 1:-**

****

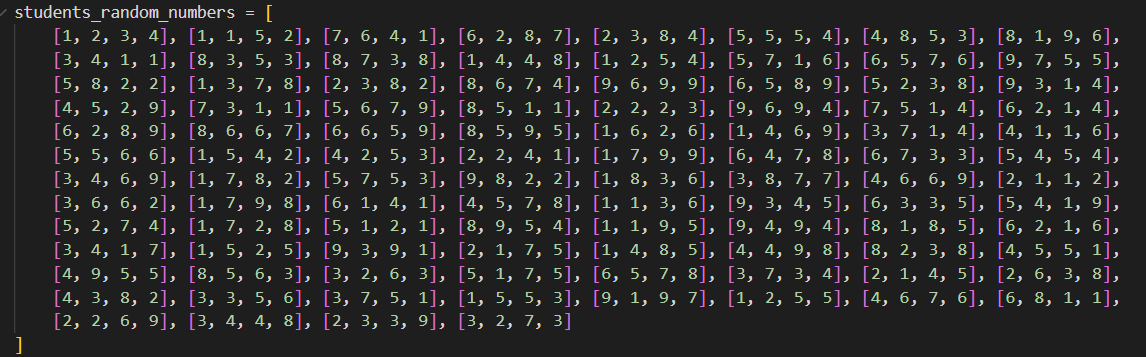
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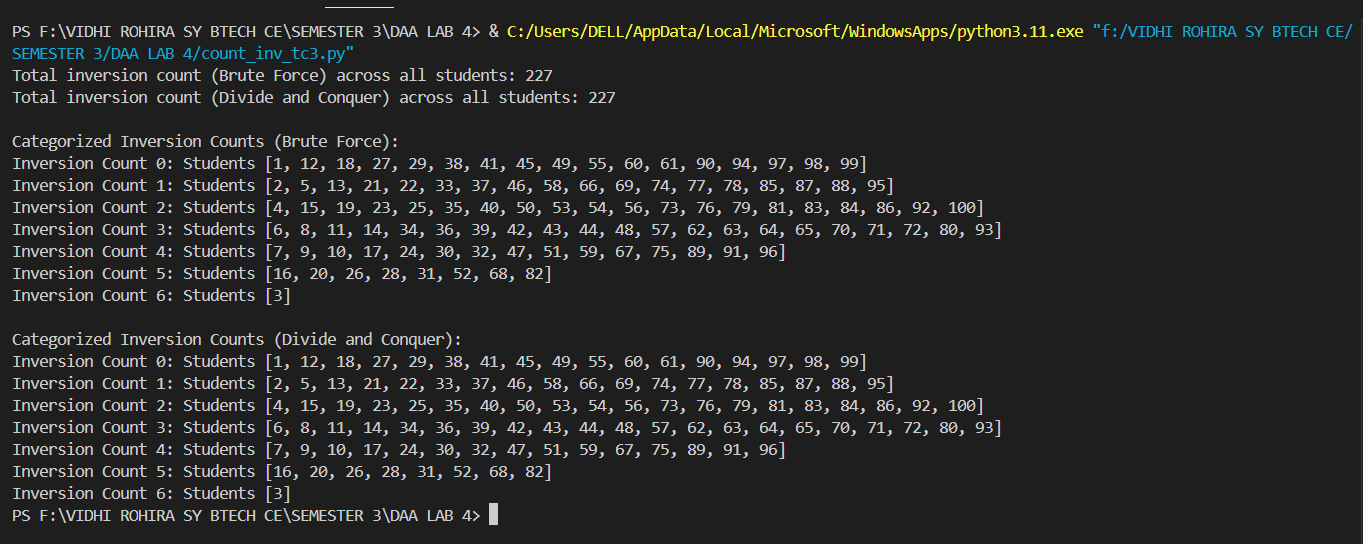
**TEST CASE 2:-**

****

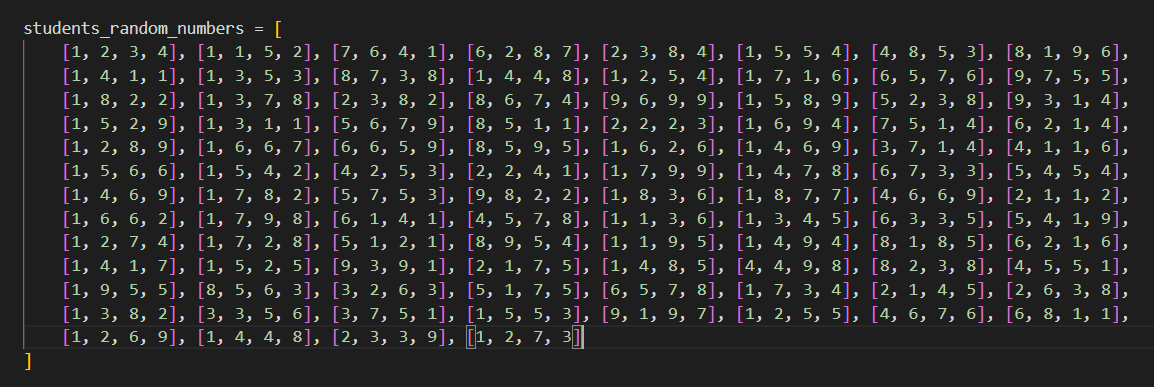
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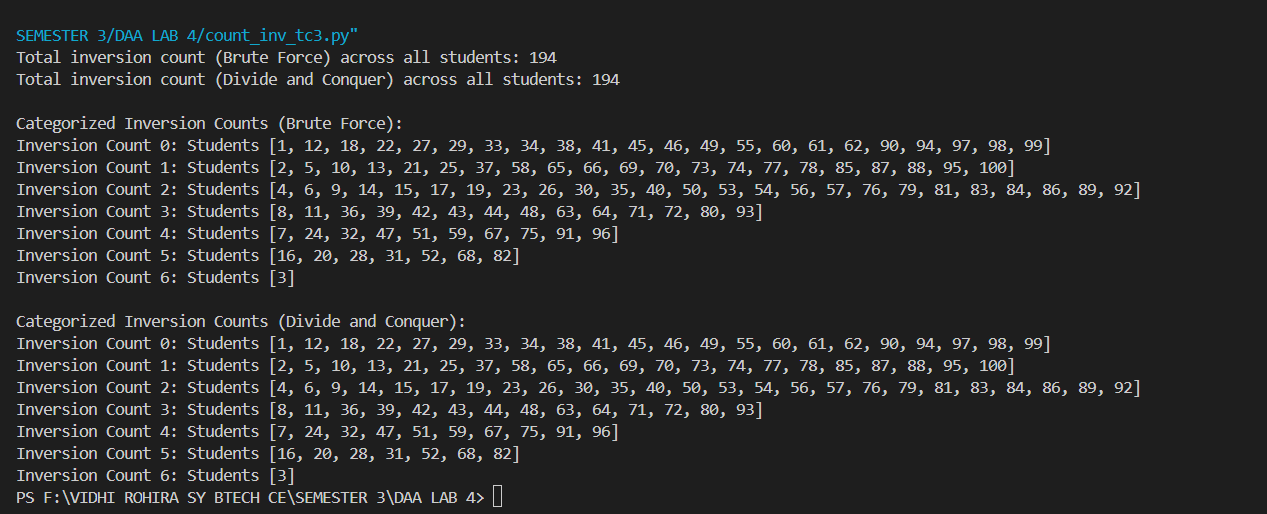
**TEST CASE 3:-**

****

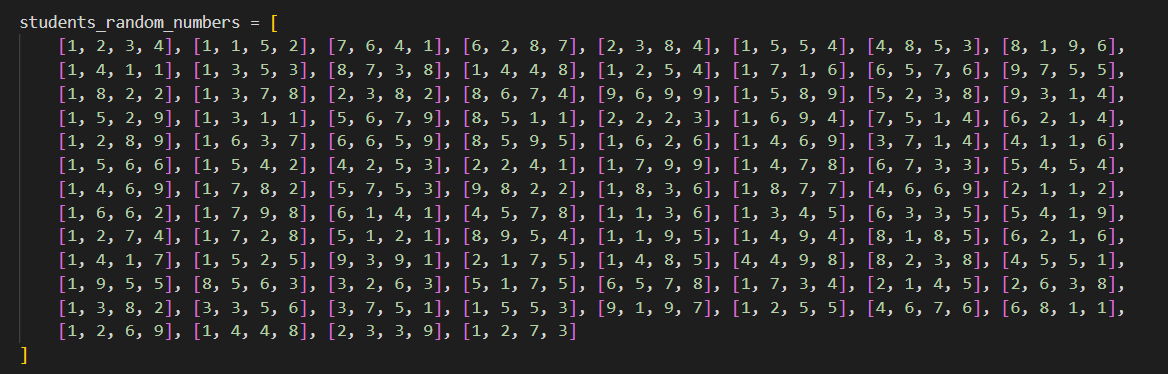
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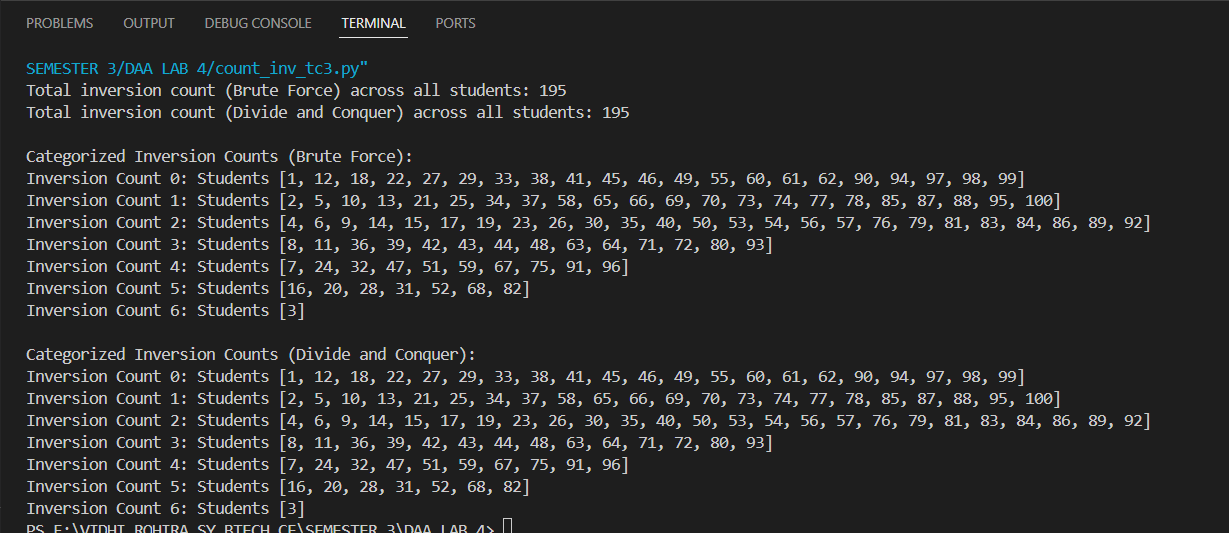
**TEST CASE 4:-**

****

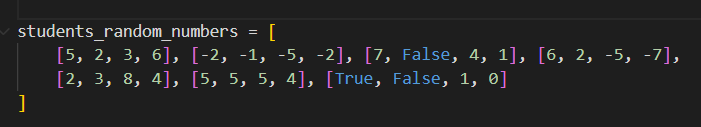
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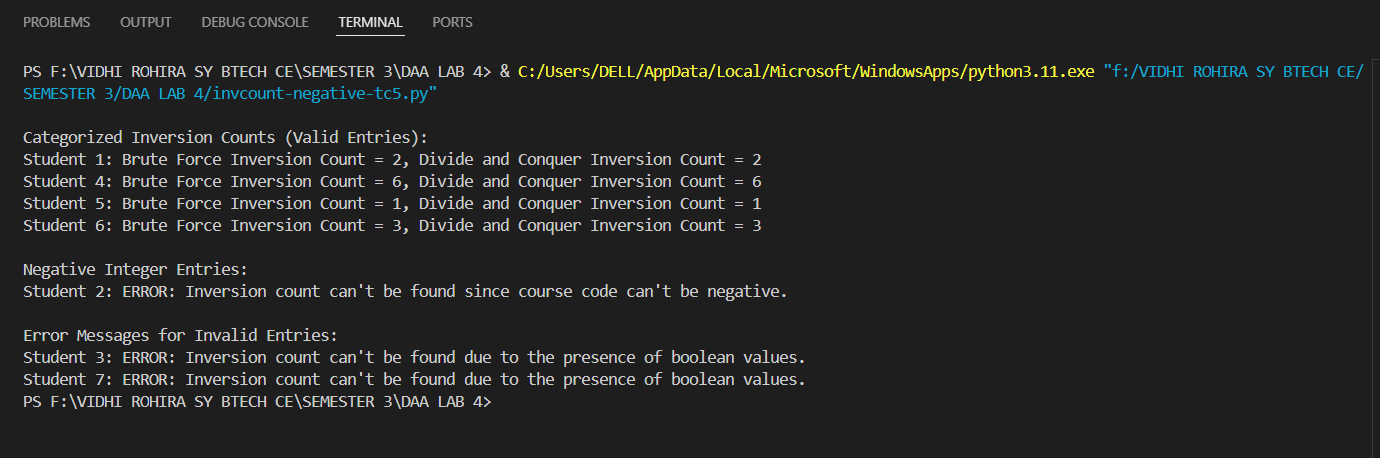
**TEST CASE 5:-**

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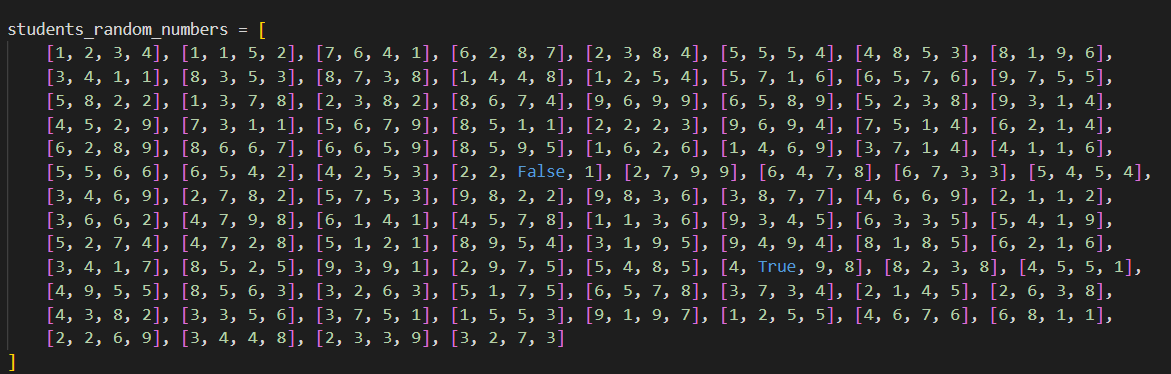
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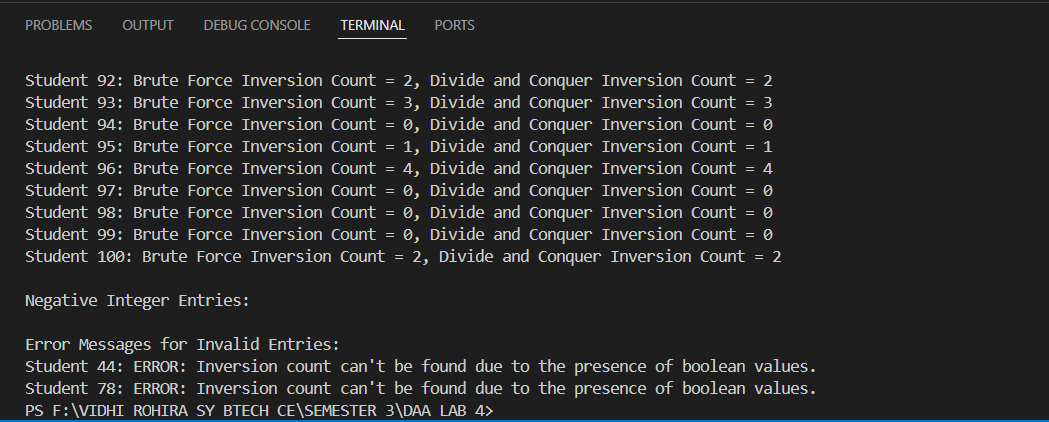
**TEST CASE 6:-**

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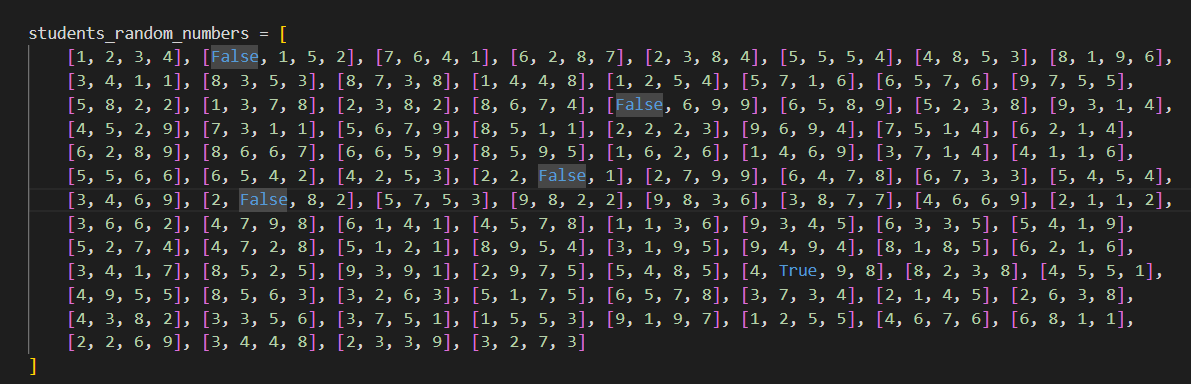
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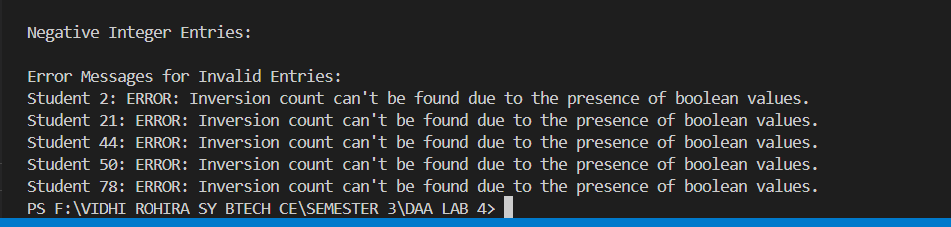
**TEST CASE 7:-**

****

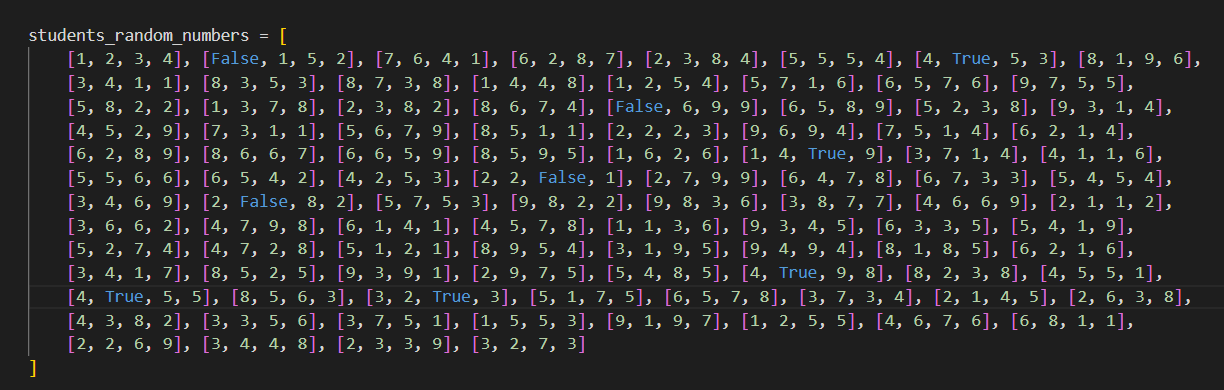
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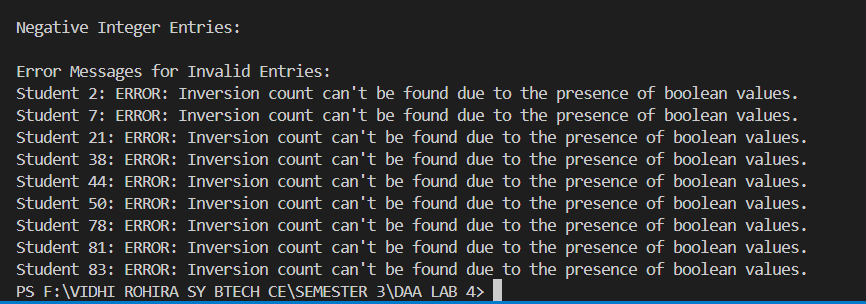
**TEST CASE 8:-**

****

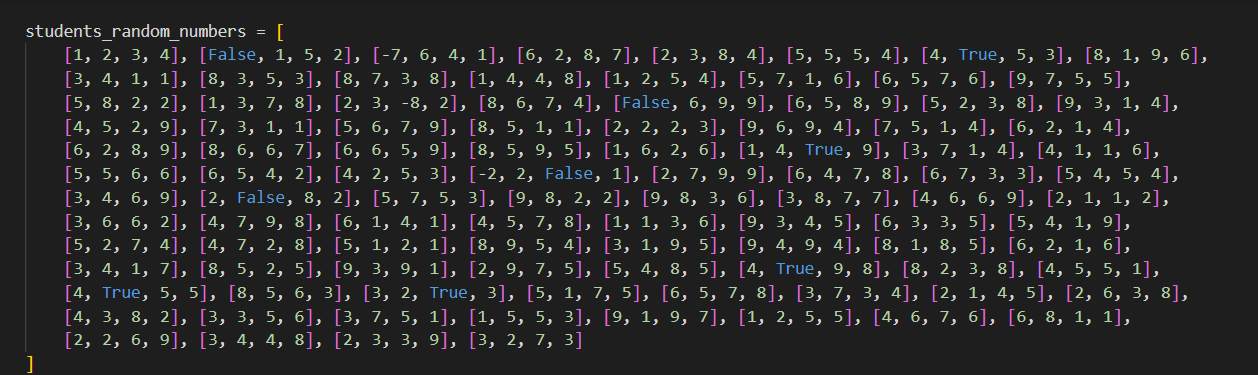
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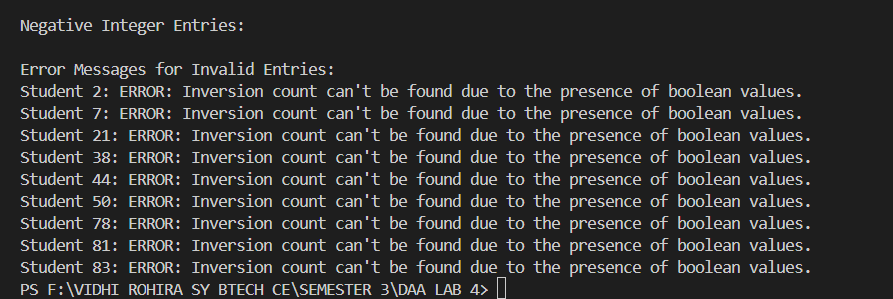
**TEST CASE 9:-**

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**TEST CASE 10:-**

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